

3 V SILICON MMIC L-BAND FREQUENCY DOWN CONVERTER

UPC8112TB

FEATURES

HIGH DENSITY SURFACE MOUNTING:
 6 Pin Super Minimold or SOT-363 Package

BROADBAND OPERATION:

RF = 0.8 to 2.0 GHz IF = 100 to 300 MHz

• INPUT IP3: -7 dBm

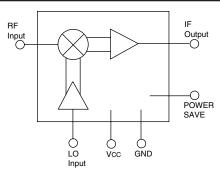
POWER SAVE FUNCTION

SUPPLY VOLTAGE: Vcc = 2.7 to 3.3 V

DESCRIPTION

NEC's UPC8112TB is a silicon RFIC manufactured using the NESAT III process. This device consists of a mixer, an IF amplifier and a LO buffer amplifier. This device is suitable as a 1st IF downconverter for the receiver stage of cellular and other wireless systems. The UPC8112TB is pin compatible and has comparable performance as the larger UPC8112T, so

INTERNAL BLOCK DIAGRAM



it is suitable for use as a replacement to help reduce system size. The IC is housed in a 6 pin super minimold or SOT-363 package.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

ELECTRICAL CHARACTERISTICS (TA = 25 °C, VCC = VPS = 3.0 V, PLO = -10 dBm)

PART NUMBER PACKAGE OUTLINE				UPC8112TB S06	
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Circuit Current (no input signal) Vcc = 3.0 V	mA	4.9	8.5	11.7
	$V_{PS} = 0.5 V$	μΑ			0.1
fRFin	RF Frequency Response	GHz	0.8	1.9	2.0
fIFout	IF Frequency Response ¹	MHz	100	250	300
CG	Conversion Gain	dB dB dB	11.5 9.5	15 13 13	17.5 15.5
NF	Single Side Band Noise Figure (SSB) frein = 900 MHz, floin = 1000 MHz frein = 1.5 GHz, floin = 1.6 GHz frein = 1.9 GHz, floin = 1.66 GHz	dB dB dB		9.0 11 11.2	11
P _{1dB}	Output Power at 1 dB gain compression, fRFin = 1.9 GHz fLOin = 1.66 GHz	dBm		-5	
Psat	Saturated Output Power fRFin = 900 MHz, fLOin = 1000 MHz fRFin = 1.9 GHz, fLOin = 1.66 GHz (PRFin = -10 dBm)	dBm dBm	-6.5 -7	-2.5 -3	
IIP3	Input 3rd Order Intercept Point, frein = 900 MHz, fLoin = 1000 MHz frein = 1.5 GHz, fLoin = 1.6 GHz frein = 1.9 GHz, fLoin = 1.66 GHz	dBm dBm dBm		-10 -9 -7	
LORF	LO Leakage at RF pin, frFin = 900 MHz, fLoin = 1000 MHz frFin = 1.5 GHz, fLoin = 1.6 GHz frFin = 1.9 GHz, fLoin = 1.66 GHz	dBm dBm dBm		-45 -46 -45	
LOIF	LO Leakage at IF pin, fRFin = 900 MHz, fLoin = 1000 MHz fRFin = 1.5 GHz, fLoin = 1.6 GHz fRFin = 1.9 GHz, fLoin = 1.66 GHz	dBm dBm dBm		-32 -33 -30	
RFLO	RF Leakage at LO Pin frin = 900 MHz, floin = 1000 MHz ² frin = 1.5 GHz, floin = 1.6 GHz ² frin = 1.9 GHz, floin = 1.66 GHz ²	dBm dBm dBm		-80 -57 -55	
Rth(JA)	Thermal Resistance (Junction to Ambient) Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB	°C/W			325

Notes:

1. External matching required.

2. PRFin = -30 dBm

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	3.6
Icc	Circuit Current	mA	77.7
PD	Power Dissipation ²	mW	200
Тор	Operating Temperature	°C	-40 to +85
Тѕтс	Storage Temperature	°C	-55 to +150

Notes:

- Operation in excess of any one of these parameters may result in permanent damage.
- 2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB ($TA = +85^{\circ}C$).

RECOMMENDED OPERATING CONDITIONS

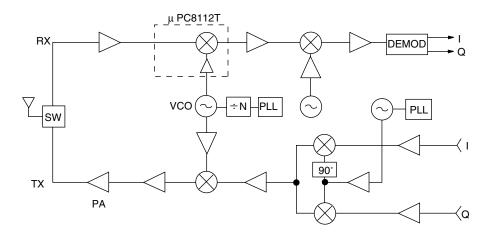
SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	2.7	3.0	3.3
Тор	Operating Temperature	°C	-40	+25	+85
PLOin	LO Input Level	dBm	-15	-10	0
fRFin	RF Input Frequency	GHz	0.8	1.9	2.0
fIFout	IF Output Frequency	MHz	100	250	300

PIN FUNCTIONS

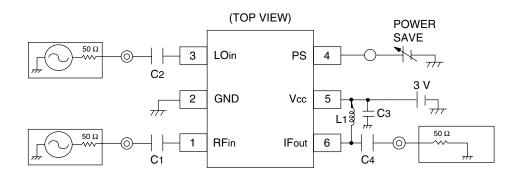
Pin No.	Symbol	Pin Voltage	Description	Internal Equivalent Circuit
5	Vcc	2.7 ~ 3.3	Supply Voltage pin. Connect a bypass capacitor (e.g., 1000 pF) to minimize ground impedance.	5
6	IFоuт	Same as Vcc voltage through external inductor	IF output pin is an open collector with high impedance. External LC matching circuit is required.	from LO AMP
1	RFIN	1.2	RF input pin to mixer. Mixer is a double balanced Gilbert cell type. Input RF signal to the pin with a $50~\Omega$ source impedance through a coupling capacitor.	
2	GND	0	Ground pin. Must be connected to the system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible to minimize ground impedance.	
3	LOIN	1.4	LO input pin to a differential buffer amplifier. Input LO signal through a coupling capacitor. Recom- mended input level: -15 to 0 dBm.	3 to mixed
4	Vps	Vcc or GND	Power-save control pin. Voltage on this pin controls ON/OFF operation as follows: Operation VPS ON ≥2.5 V OFF 0-0.5 V	(2)————————————————————————————————————

TYPICAL APPLICATION EXAMPLE

PCS or DIGITAL CELLULAR



TEST CIRCUIT



Note: 1. C1, C2, C3 are 1,000 pF capacitors. 2. L1 and C4 are matching elements. L1 = 100nH and C4 = 2.7 pF for fiF = 240 MHz

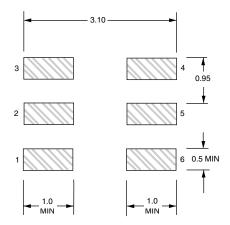
OUTLINE DIMENSIONS (Units in mm)

2.0±0.2 0.65 3 1.3 0.65 2 0.9 ± 0.1 0.9 ± 0.1 0.7 0.9 ± 0.1 0.7 0.9 ± 0.1 0.7 0.9 ± 0.1 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.15 ± 0.5 0.

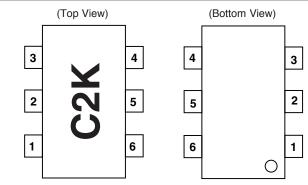
RECOMMENDED P.C.B. LAYOUT (Units in mm)

Note:

All dimensions are typical unless otherwise specified.



LEAD CONNECTIONS



- 1. RFIN
- 2. GND
- 3. LOIN
- 4. PS
- 5. Vcc
- 6. IFout

ORDERING INFORMATION

PART NUMBER	QTY
UPC8112TB-E3-A	3K/Reel

Note:

Embossed tape, 8 mm wide. Pins 1, 2, 3 are in tape pull-out direction.

Life Support Applications

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CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)		
Mercury	< 1000 PPM	Not De	etected	
Cadmium	Cadmium < 100 PPM Not Dete		etected	
Hexavalent Chromium	< 1000 PPM	Not De	etected	
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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